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Thanh, C.K.; Lewis, S.H.; Hurst, P.J.;

Solid-State Circuits, IEEE Journal of , Volume: 32 , Issue: 8 , Aug. 1997

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2 Granular quantization noise in the first-order delta-sigma modulator

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3 A 2nd-order double-sampled $\Delta\Sigma$ modulator with individual-level averaging

Thanh, C.; Lewis, S.H.; Hurst, P.J.;

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4 Design techniques for CMOS micropower low-voltage switched-capacitor delta-sigma modulator

Tsung-Sum Lee; Wen-Bin Lin; Dung-Lin Lee;

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6 IF-sampling fourth-order bandpass /spl Delta//spl Sigma/ modulator for digital receiver applications

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8 A 90-dB SNR 2.5-MHz output-rate ADC using cascaded multibit delta sigma modulation at 8x oversampling ratio

Fujimori, I.; Longo, L.; Hairapetian, A.; Seiyama, K.; Kasic, S.; Jun Cao; Shu-Chan;

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2	<input type="checkbox"/>	<input type="checkbox"/>	US 6741123 B	20040722	10	Delta-sigma modulator for e.g. digital audio amplifier, has compensation circuitry compensating voltage variations based on measured average and				ANDERSEN, J et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US

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